

# Engineering

## Bachelor of Engineering (Honours) (3707)

### Telecommunications Engineering (TELEAH)

## T1 Entry 2024 Sample Plan



**UNSW**  
SYDNEY

Year 1		Year 2		Year 3		Year 4	
Term 1	<b>DESN1000</b> Intro. to Eng. Design and Innovation	Term 1	<b>ELEC2141</b> Digital Circuit Design	Term 1	<b>ELEC3115</b> Electromagnetic Engineering	Term 1	<b>ELEC4122</b> Strategic Leadership & Ethics
	<b>PHYS1121</b> Physics 1 A <u>OR</u> <b>PHYS1131</b> Higher Physics 1A		<b>ELEC2134</b> Circuits and Signals		<b>ELEC3106</b> Electronics		<b>Discipline Elective</b>
	<b>MATH1131</b> Mathematics 1A <u>OR</u> <b>MATH1141</b> Higher Mathematics 1A				<b>TELE3113</b> Analogue & Digital Communications		<b>ELEC4951</b> Research Thesis A (4 UoC)
Term 2	<b>COMP1511</b> Programming Fundamentals <u>OR</u> <b>COMP1911</b> Computing 1A	Term 2	<b>DESN2000</b> Engineering Design & Professional Practice	Term 2	<b>ELEC3117</b> Electrical Engineering Design	Term 2	<b>ELEC4952</b> Research Thesis B (4 UoC)
	<b>MATH1231</b> Mathematics 1B <u>OR</u> <b>MATH1241</b> Higher Mathematics 1B		<b>MATH2099</b> Mathematics 2B		<b>ELEC3114</b> Control Systems		<b>Free Elective</b> <u>OR</u> <b>Discipline Elective</b>
			<b>ELEC2133</b> Analogue Electronics		<b>Discipline Elective</b>		<b>General Education Course</b>
Term 3	<b>COMP1521</b> Computer Systems Fundamentals	Term 3	<b>ELEC3104</b> Digital Signal Processing	Term 3	<b>General Education Course</b>	Term 3	<b>ELEC4123</b> Electrical Design Proficiency
	<b>PHYS1231</b> Higher Physics 1B		<b>TELE3118</b> Network Technologies		<b>Free Elective</b> <u>OR</u> <b>Discipline Elective</b>		<b>TELE3119</b> Trusted Networks
	<b>ELEC1111</b> Electrical Circuit Fundamentals		<b>MATH2069</b> Mathematics 2A				<b>ELEC4953</b> Research Thesis C (4 UoC)

### NOTES

Compulsory Training Component: There is a program requirement of 60 days approved [Industrial Training](#) ENGG4999

**This is intended as a guide only. Courses do not need to be studied in the exact structure that they appear here.**

# Engineering

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### Telecommunications Engineering (TELEAH)

## T2 Entry 2024 Sample Plan



Year 1		Year 2		Year 3		Year 4	
Term 2	<b>COMP1511</b> Programming Fundamentals <u>OR</u> <b>COMP1911</b> Computing 1A	Term 2	<b>DESN2000</b> Engineering Design & Professional Practice	Term 2	<b>ELEC3117</b> Electrical Engineering Design	Term 2	<b>Discipline Elective</b>
	<b>MATH1131</b> Mathematics 1A		<b>MATH2099</b> Mathematics 2B		<b>ELEC3114</b> Control Systems		<b>ELEC4951</b> Research Thesis A (4 UoC)
	<b>PHYS1121</b> Physics 1 A <u>OR</u> <b>PHYS1131</b> Higher Physics 1A		<b>ELEC2133</b> Analogue Electronics		<b>General Education Course</b>		
Term 3	<b>DESN1000</b> Intro. to Eng. Design and Innovation	Term 3	<b>ELEC3104</b> Digital Signal Processing	Term 3	<b>Free Elective <u>OR</u> Discipline Elective</b>	Term 3	<b>ELEC4123</b> Electrical Design Proficiency
	<b>ELEC1111</b> Electrical Circuit Fundamentals		<b>MATH2069</b> Mathematics 2A		<b>TELE3118</b> Network Technologies		<b>TELE3119</b> Trusted Networks
	<b>MATH1231</b> Mathematics 1B						<b>ELEC4952</b> Research Thesis B (4 UoC)
Term 1	<b>ELEC2134</b> Circuits and Signals	Term 1	<b>COMP1521</b> Computer Systems Fundamentals	Term 1	<b>TELE3113</b> Analogue & Digital Communications	Term 1	<b>ELEC4122</b> Strategic Leadership & Ethics
	<b>ELEC2141</b> Digital Circuit Design		<b>ELEC3115</b> Electromagnetic Engineering		<b>General Education Course</b>		<b>Discipline Elective</b>
	<b>PHYS1231</b> Higher Physics 1B		<b>ELEC3106</b> Electronics		<b>Free Elective <u>OR</u> Discipline Elective</b>		<b>ELEC4953</b> Research Thesis C (4 UoC)

<b>NOTES</b>	<p>Compulsory Training Component: There is a program requirement of 60 days approved <a href="#">Industrial Training</a> ENGG4999</p> <p><b>This is intended as a guide only. Courses do not need to be studied in the exact structure that they appear here.</b></p>
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# Engineering

## Bachelor of Engineering (Honours) (3707)

### Telecommunications Engineering (TELEAH)

## T3 Entry 2024 Sample Plan



Year 1		Year 2		Year 3		Year 4	
Term 3	<b>PHYS1121</b> Physics 1 A <u>OR</u> <b>PHYS1131</b> Higher Physics 1A	Term 3	<b>PHYS1231</b> Higher Physics 1B	Term 3	<b>TELE3118</b> Network Technologies	Term 3	<b>TELE3119</b> Trusted Networks
	<b>COMP1511</b> Programming Fundamentals		<b>General Education Course</b>		<b>ELEC3104</b> Digital Signal Processing		<b>Discipline Elective</b>
	<b>MATH1131</b> Mathematics 1A <u>OR</u> <b>MATH1141</b> Higher Mathematics 1A		<b>MATH2069</b> Mathematics 2A		<b>Free Elective</b> <u>OR</u> <b>Discipline Elective</b>		<b>ELEC4951</b> Research Thesis A (4 UoC)
Term 1	<b>ELEC1111</b> Electrical Circuit Fundamentals	Term 1	<b>ELEC2141</b> Digital Circuit Design	Term 1	<b>ELEC3115</b> Electromagnetic Engineering	Term 1	<b>ELEC4122</b> Strategic Leadership & Ethics
	<b>DESN1000</b> Intro. to Eng. Design and Innovation		<b>ELEC2134</b> Circuits and Signals		<b>ELEC3106</b> Electronics		<b>ELEC4123</b> Electrical Design Proficiency
	<b>MATH1231</b> Mathematics 1B <u>OR</u> <b>MATH1241</b> Higher Mathematics 1B		<b>General Education Course</b>		<b>TELE3113</b> Analogue & Digital Communications		<b>ELEC4952</b> Research Thesis B (4 UoC)
Term 2	<b>COMP1521</b> Computer Systems Fundamentals	Term 2	<b>DESN2000</b> Engineering Design & Professional Practice	Term 2	<b>ELEC3117</b> Electrical Engineering Design	Term 2	<b>Free Elective</b> <u>OR</u> <b>Discipline Elective</b>
	<b>MATH2099</b> Mathematics 2B		<b>ELEC2133</b> Analogue Electronics		<b>ELEC3114</b> Control Systems		<b>Discipline Elective Course</b>
							<b>ELEC4953</b> Research Thesis C (4 UoC)

<b>NOTES</b>	<p>Compulsory Training Component: There is a program requirement of 60 days approved <a href="#">Industrial Training</a> ENGG4999</p> <p><b>This is intended as a guide only. Courses do not need to be studied in the exact structure that they appear here.</b></p>
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